



www.gomactech.net

GOMACTech-2025

“MICROELECTRONICS Full Throttle”

Pasadena, CA
March 17 – 20, 2025

Call for Papers

Established in 1968, GOMACTech is the premier forum for reviewing and reporting on developments in microcircuit technologies and applications for Government systems as well as announcements and updates of major government microelectronics and semiconductor initiatives. **GOMACTech is an unclassified, export-controlled event.** All registrants **must provide proof** of U.S. citizenship or permanent residence status and sign a non-disclosure agreement prior to being permitted entry into the conference.

GOMACTech-2025 features plenary sessions addressing the theme of **MICROELECTRONICS Full Throttle**, as well as a technical program describing the latest technological advances, including trustworthy and cyber-secure components/technologies, electro-optical components, RF components, micro-nano electronics, electronics integration, electronics materials, emerging neuromorphic electronics, quantum information/sensing technologies and technologies beyond Moore’s law, advances in wide and ultrawide-bandgap materials and device development, and novel power electronics.

Authors whose abstract is accepted—for oral and poster presentations—are required to submit a complete paper for the conference proceedings. Abstracts are solicited in the technical topic areas listed below. For additional, detailed descriptions of the technical topic areas, please go to the [GOMACTech website](#) → Paper Submission → Technical Topics.

[Submit abstracts here.](#)

We are looking forward to your contributions!

Technical Topic Areas

Advanced Materials and Processes

Emerging Technologies

EO/IR Technologies, Components, and Systems

High Performance Digital and Mixed-signal Technologies

Packaging, Integration, Thermal & Control Technologies

Photonic Technologies, Components, and Systems

Power Electronics & Emerging Power Technologies

Radiation Hardened Technologies, Designs, and Systems

RF Technologies, Components, and Systems

Trusted, Assured and Cyber-secure Microelectronics

2-page Abstract Due	Monday, September 9, 2024
Author Notification	Friday, November 8, 2024
Final Full Paper Due	Friday, January 24, 2025

Vipul J. Patel, General Chair
US Department of the Air Force
Vipul.Patel@us.af.mil

Wayne A. Churaman, Technical Program Chair
US DEVCOM Army Research Laboratory
Wayne.A.Churaman.civ@army.mil

David J. Meyer, Local Arrangement Chair
US Naval Research Laboratory
David.J.Meyer90.civ@us.navy.mil